

FORM PTO-892 (REV. 3-78) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				SERIAL NO. 843454	GROUP ART UNIT 114	ATTACHMENT TO PAPER NUMBER 6				
NOTICE OF REFERENCES CITED				APPLICANT(S) Blanchard.						
U.S. PATENT DOCUMENTS										
*	DOCUMENT NO.			DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE		
A	4520552	6-85	Armand	29	580					
B	4364074	12-82	Garmache et al.	148	188					
C	4554728	12-85	SHEPARD	148	016.50					
D	4353086	10-82	Jaccodine et al.	29	580					
E	4455740	6-84	Iwai	148	014.50					
F	3518509	6-70	Cullis	357	23.4					
G	4631803	12-86	Hunter et al.	29	578					
H	3412297	11-68	Amelung	357	23.4					
I	4571815	2-86	Baliga et al.	29	578					
J	4528047	7-85	Beyer et al.	148	175					
K	3500139	3-70	Fruin et al.	148	024.50					
FOREIGN PATENT DOCUMENTS										
*	DOCUMENT NO.			DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS DWG	PP. SPEC.
L	0164095	12-85	EP.	Benjamin	357	23.4				
M	0003287	1-83	Japan	Furumura	357	23.4				
N	0050752	3-83	Japan	Fukuyama	29	576W				
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)										
R	Ghandhi, "VLSI Fabrication principles", John Wiley & Sons, N.Y. 1983 pp. 582-585									
S										
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EXAMINER	T. Thomas			DATE	6-9-87					
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)										